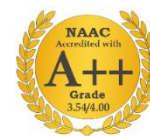





SETHU INSTITUTE OF TECHNOLOGY

(An Autonomous Institution| Accredited with 'A++' Grade by NAAC)

Pulloor, Kariapatti –Taluk. Virudhunagar Dist-626115.



Department of Electrical and Electronics Engineering			
Name	Dr. NARMADHA G		
Date of Birth	28.12.1986		
Unique ID	1-3362861130		
Educational Qualifications	M.E.,Ph.D.,		
Designation	Associate Professor		
Email ID	gnarmadhame@gmail.com		
Alternate Email ID	gnarmadha@sethu.ac.in		
Experience	Teaching	Total	
	16 Years 2 Months	16 Years 2 Months	
Date of Joining the Institution	11.01.2016		
Area of Specialization	VLSI		
Courses taught	<ul style="list-style-type: none">➤ VLSI Design➤ Digital Logic Circuits➤ Machine Learning➤ Basic Electrical and Electronics Engineering➤ Microprocessors and Microcontroller Programming➤ Analog Electronics➤ Embedded Systems		
Research Focus	Biomedical Image Processing with VLSI implementation, Cryptography, Deep Learning		
Research guidance (Number of Scholars)	4		
Subject Competency	Digital Systems, VLSI Design		
No. of papers published	National Journals	International Journals	Conferences
	1	14	4
PG Specialization	Applied Electronics		
Ph.D. Specialization	VLSI		
Projects Carried out	<ul style="list-style-type: none">➤ Project titled “Design and Implementation of IOT based Smart Bin for Organic Fertilizer Generation” is approved by Unnat Bharat Abhiyan (UBA 2.0) and sanctioned Rs. 1 Lakh.➤ Mentor for the Project Titled “Accident Alerting System” approved by MSME 2.0 and sanctioned Rs.10.35 Lakhs.➤ Won 10 Lakhs in MSME Women Hackathon 3.0 for the project “AI based Automatic Breast Cancer Detector” in 2024.		
Patents (Filed & Granted)	1		



Academic Credentials

Level	Degree	Specialization	University	Year of Completion
UG	B.E	Electrical and Electronics Engineering	Pandian Saraswathi Yadav Engg. College/ Anna University	2007
PG	M.E	Applied Electronics	Thanthai Periyar Govt. Institute of Technology, Vellore / Anna University	2009
Ph.D.	Ph.D.	Information & Communication	Anna University, Chennai	2018

Details of Journal Publication:

Sl.No	Year	Journal / Conference	Paper Title	Publication Details
1	2011-12	International Journal Of Research In Advanced Electronics	Fixed Width Modified Booth Multiplier For High Accuracy	April, 2012
2	2013-14	International Journal of Computer Applications	An Efficient Multi-Path Delay Commutator Architecture	Volume 98 Issue 17
3	2015-16	International Journal of Applied Engineering Research	Design and Implementation of Time Efficient Carry Select Adder using FPGA	ISSN 0973-4562 Volume 10 Issue Number 3 (2015) pp. 7215-7227
4	2015-16	International Journal of Advanced Engineering Technology	An optimized Montgomery Multiplier for Public Key Cryptography	Vol. VII/Issue I/Jan.-March, 2016
5	2016-17	Asian Journal of Research in Social Sciences and Humanities	VLSI Implementation of RSA Cryptosystems using Montgomery Multiplication with Sum Based Adder	Vol. 7/Issue 2/Feb., 2017 pp. 632-639
6	2018-19	7th International Conference on Smart Computing & Communications (ICSCC)	Carry Based Approximate full adder for low power approximate computing	Curtin University, Malaysia, 28th – 30th June 2019

Sl.No .	Year	Journal / Conference	Paper Tile	Publication Details
7.	2019-20	International Journal on Emerging Technologies	Design of Smart Pulse Oximeter using ATMEGA 328 Microcontroller	Volume. 11, Issue .3, pp. 696-700, May 2020
8.	2021-22	ICT Express - Elsevier	Performance of implantable antenna at ISM band characteristics for biomedical base	Vol.8, Issue 2, pp.198-201, 2022
9.	2021-22	Journal of Engineering Research	A Low Power and High Speed Approximate Adder for Image Processing Applications	Vol.10, Issue 1A, pp.150-160, 2022
10.	2022-23	Journal of Engineering Research	Epileptic EEG signal classifications based on DT-CWT and SVM classifier	Vol.10, Issue 2A, pp.95-104, 2022
11.	2023-24	Mathematical Problems in Engineering	Detection of Human Stress Using Optimized Feature Selection and Classification in ECG Signals	Vol. 23, pp.1-8
12.	2023-24	International Journal of Informatics and Communication Technology (IJ-ICT)	Hen maternal care inspired optimization framework for attack detection in wireless smart grid network	Vol.13, Issue 1, pp.123-130
13.	2023-24	Journal of Engineering Science and Technology	Design of Storing and Restoring Array Divider Circuit Using Binary Decision Diagram-Based Adder/ Subtractor Circuit	Vol. 19, No. 4 (2024) 1235 - 1253
14.	2024 - 25	Results in Engineering	Enhancing spectrum prediction in cognitive radio networks using an optimized generative adversarial network	Vol.26, pp.1-10

Details of Conference attended:

Sl.No .	Year	Journal / Conference	Paper Tile	Publication Details
1	2010-11	International Conference on Computer and Computer Intelligence (ICCCI 2011)	Realization of Generalized Parallel Counter for Improving FPGA Performance	December 2-3, 2011
2	2023 - 24	National Conference on “Emerging Trends in Engineering and Technology” conducted by National Institute of Technology, Pondicherry	Accident Prediction on Loaded/Unloaded Vehicle	15.02.2024
3	2024 - 25	3rd International Conference on Applications of AI in Smart Technologies and Manufacturing	Micro-Irrigation System with Automated Valve using IoT	08.03.2025

Sl.No .	Year	Journal / Conference	Paper Tile	Publication Details
4	2024 - 25	12th International Conference on Emerging Trends in Engineering and Technology	Hybrid GAN-Based Battery Health Estimation in Energy Storage Systems Using MultiChannel Charging Profiles for EVs in Automotive Applications	04-04-2025

Details of Patents Filed and Granted:

Application No	Title of the Patent	Status
202441081705	Design and Implementation of IoT based Smart Bin for Organic Fertilizer Generation	Published

Expert Lecture Delivered:

Title of the Lecture	Venue	Date
Hands-on training using micro wind software for IC design	UCSI University / Malaysia	07-06-2024
Real Time Applications in Logic Design and Verification	Saveetha University	13-05-2025